OTPE 1805

28/2

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8

by certify that this correspondence is being deposited with the United States Postal Service as first class mail, with sufficient postage, in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Riexandria, VA 22313-1450, on the belowdate:

ate: 10/12/08 Name: Anthony P. Curtis, Ph.D., 46,193 Signature:

BRINKS HOFER GILSON &LIONE

IN	THE	UNITED	STATES	PATENT	AND TR	RÁDEMARK	OFFICE

In re Appln. of: Karlheinz Müller et al.

Appln. No.:

10/526,818

Filed:

March 3, 2005

For:

METHOD FOR FABRICATING AN INTEGRATED PIN DIODE AND

ASSOCIATED CIRCUIT ARRANGEMENT

Attorney Docket No:

10808/222

An additional filing fee has been calculated as shown below:

Attn: Application Processing Division Customer Correction Branch

Commissioner for Patents

P. O. Box 1450

Alexandria, VA 22313-1450

TRANSMITTAL

Art Unit: 2812

Examiner: Not yet assigned

Sir:

Attac	hed i	is/	are:
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M	Transmittal Cover Letter (1p. Filed in Dup.); Request for Correction of Filing Receipt (2pp. Filed in Dup.); Copy of Filing Receipt with Corrections (3pp.); Copy of Previously Filed Declaration
\boxtimes	Return Receipt Postcard
Fee o	calculation:
\boxtimes	No additional fee is required.
	Small Entity.
	An extension fee in an amount of \$ for amonth extension of time under 37 C.F.R. § 1.136(a).
П	A petition or processing fee in an amount of \$ under 37 C.F.R. § 1.17().

			Sma	Small Entity		Not a Small Entity			
	Claims Remaining After Amendment		Highest No. Previously Paid For	Present Extra	Rate	Add'l Fee	or	Rate	Add'i Fee
Total		Minus			x \$25=			x \$50=	
Indep.		Minus			x 100=			x \$200=	
First Pre	esentation of Multiple D	ep. Claim	1		+\$180=			+ \$360=	
					Total	\$		Total	\$

First I	Presentation of Multiple Dep. Claim		+\$180=		+ \$360=	
			Total	\$	Total	\$
Fee	payment:					
	A check in the amount of \$ is enclosed.					
	Please charge Deposit Account No. 23-1925 in the enclosed for this purpose.	he amount o	f\$	A copy of	this Transmitt	al is
	Payment by credit card in the amount of \$	(Form PTO-	2038 is a	ttached).		
	The Director is hereby authorized to charge pay § 1.16 and any patent application processing (including any extension fee required to ensoverpayment, to Deposit Account No. 23-1925.	fees under sure that th	37 CFR	§ 1.17 as is timely	sociated with	this paper
Dat	19/2/00	//	Z		ea No. 46 193	
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Patent Application Serial No. 10/526,818 Our File No. 10808/222

> I hereby certify that this correspondence is being deposited with the United States Postal Service, with sufficient postage, as first class mail in an envelope addressed to:

Commissioner for Patents, P.O. Box 1450, Alexandria,

VA 22313-1450

on October 12, 2005

Date of Deposit

Anthony P. Curtis, Ph.D., Reg No. 46,193

Name of applicant, assignee or Registered Representative

Signature

Date of Signature

Our Case No. 10808/222

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Karlheinz Müller et al.

Serial No. 10/526,818

Filing Date: March 3, 2005

METHOD FOR FABRICATING AN

INTEGRATED PIN DIODE AND

ASSOCIATED CIRCUIT

ARRANGEMENT

Not yet assigned Examiner

Group Art Unit No. 2812

REQUEST FOR CORRECTION OF FILING RECEIPT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Application Processing Division Attention:

Customer Correction Branch

Sir:

Applicant requests the issuance of a corrected filing receipt (copy enclosed) for the above-referenced patent application, and in support of this request respectfully states:

U.S. Patent Application Serial No. 10/526,818 Our File No. 10808/222

The title should be "METHOD FOR FABRICATING AN INTEGRATED PIN DIODE AND ASSOCIATED CIRCUIT ARRANGEMENT" not "METHOD FOR PRODUCING AN INTEGRATED PIN DIODE AND CORRESPONDING CIRCUIT" (please see attached Declaration previously filed)

The Commissioner is hereby authorized to charge any fees required to Deposit Account No. 23-1925. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

Anthony P. Curtis, Ph.D. Registration No. 46,193

Agent for Applicant

BRINKS HOFER GILSON & LIONE P.O. BOX 10395 CHICAGO, ILLINOIS 60610 (312) 321-4200

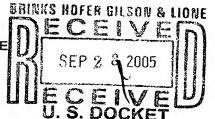


UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address COMMISSIONER FOR PATENTS P.O. DOX 1430 Alexandria, Vinginia 22313-1450

FILING OR 371 APPL NO. ART UNIT FIL FEE REC'D ATTY.DOCKET NO DRAWINGS TOT CLMS IND CLMS (c) DATE 10/526.818 03/03/2005 2812 900 2 10808/222 20 2

48581 **BRINKS HOFER GILSON & LIONE** INFINEON PO BOX 10395

CHICAGO, IL 60610



CONFIRMATION NO. 5473 FILING RECEIPT OC000000016994676*

Date Mailed: 09/15/2005

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please mail to the Commissioner for Patents P.O. Box 1450 Alexandria Va 22313-1450. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Karlheinz Muller, Velden, GERMANY; Johannes Karl Sturm, Villach, AUSTRIA;

Power of Attorney: The patent practitioners associated with Customer Number 48581.

Domestic Priority data as claimed by applicant

This application is a 371 of PCT/DE03/02740 08/14/2003

Foreign Applications

GERMANY 102 41 156.5 09/05/2002

Projected Publication Date: 12/22/2005

Non-Publication Request: No

Early Publication Request: No

Title

Method for producing an integrated pin diode and corresponding circuit

Method For Fabricating on Integrated Pin Diode and Associated Circuit Arrangement

438

PROTECTING YOUR INVENTION OUTSIDE THE UNITED STATES

Since the rights granted by a U.S. patent extend only throughout the territory of the United States and have no effect in a foreign country, an inventor who wishes patent protection in another country must apply for a patent in a specific country or in regional patent offices. Applicants may wish to consider the filing of an international application under the Patent Cooperation Treaty (PCT). An international (PCT) application generally has the same effect as a regular national patent application in each PCT-member country. The PCT process **simplifies** the filing of patent applications on the same invention in member countries, but **does not result** in a grant of "an international patent" and does not eliminate the need of applicants to file additional documents and fees in countries where patent protection is desired.

Almost every country has its own patent law, and a person desiring a patent in a particular country must make an application for patent in that country in accordance with its particular laws. Since the laws of many countries differ in various respects from the patent law of the United States, applicants are advised to seek guidance from specific foreign countries to ensure that patent rights are not lost prematurely.

Applicants also are advised that in the case of inventions made in the United States, the Director of the USPTO must issue a license before applicants can apply for a patent in a foreign country. The filing of a U.S. patent application serves as a request for a foreign filing license. The application's filing receipt contains further information and guidance as to the status of applicant's license for foreign filing.

Applicants may wish to consult the USPTO booklet, "General Information Concerning Patents" (specifically, the section entitled "Treaties and Foreign Patents") for more information on timeframes and deadlines for filing foreign patent applications. The guide is available either by contacting the USPTO Contact Center at 800-786-9199, or it can be viewed on the USPTO website at http://www.uspto.gov/web/offices/pac/doc/general/index.html.

For information on preventing theft of your intellectual property (patents, trademarks and copyrights), you may wish to consult the U.S. Government website, http://www.stopfakes.gov. Part of a Department of Commerce initiative, this website includes self-help "toolkits" giving innovators guidance on how to protect intellectual property in specific countries such as China, Korea and Mexico. For questions regarding patent enforcement issues, applicants may call the U.S. Government hotline at 1-866-999-HALT (1-866-999-4158).

LICENSE FOR FOREIGN FILING UNDER Title 35, United States Code, Section 184 Title 37, Code of Federal Regulations, 5.11 & 5.15

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NOT GRANTED

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DECLARATION FOR PATENT APPLICATION, POWER OF ATTORNEY & DESIGNATION OF CORRESPONDENCE ADDRESS

As below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

METHOD FOR FABRICATING AN INTEGRATED PIN DIODE AND ASSOCIATED CIRCUIT ARRANGEMENT

the specification of which (check one)
X is attached hereto.
was filed on as Application Serial No
and was amended on (if applicable).
I hereby state that I have reviewed and understand the contents of the above-identified specification including the claims, as amended by any amendment referred to above.
I acknowledge the duty to disclose information known to me to be material to the patentability of this application in accordance with 37 CFR §1.56(a).
PRIOR FOREIGN/PCT APPLICATION(S) FILED WITHIN 12 MONTHS (6 MONTHS FOR DESIGN) PRIOR TO THIS APPLICATION AND ANY PRIORITY CLAIMS UNDER 35 U.S.C. § 119(a)-(d)
I hereby claim foreign priority benefits under 35 USC § 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Pr	ior Foreign App	lication(s)	Priority (Claimed
Number	Country	Day/Month/Year Filed	Yes	No
102 41 156.5	DE	05/09/2002	X	
•				
		·		

CLAIM FOR BENEFIT OF PRIOR U.S. PROVISIONAL APPLICATION(S) (35 U.S.C. § 119(e))

I hereby claim the benefit under Title 35, United States Code, § 119 (e) of any United States provisional application(s) listed below:

Provisional Application Number	Filing Date

CLAIM FOR BENEFIT OF EARLIER US/PCT APPLICATION(S) UNDER 35 U.S.C. § 120

I hereby claim the benefits under 35 USC § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of 35 USC § 112, I acknowledge the duty to disclose material information as defined in 37 CFR § 1.56 which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

Appl. No.	Filing Date	Patented, Pending, Abandoned
PCT/DE03/02740	August 14, 2003	Pending

<u>Power of Attorney</u>: As a named inventor, I hereby appoint the Practitioners associated with the following Customer Number as its attorneys, with full power of substitution and revocation, to prosecute this application and any continuations, divisions, reissues, and reexaminations thereof, to receive the patent(s), to transact all business in the United States Patent and Trademark Office connected therewith and to act on INVENTOR'S behalf before the competent International Authorities in connection with any and all international applications filed.

Customer No. 48581- Infineon Technologies

Please recognize or change the correspondence address for this application to the address associated with the above-mentioned Customer Number. Please direct all telephonic and facsimile communications to:

Anthony P. Curtis, Ph.D. Tel.: (312) 321-4200; Fax: (312) 321-4299

I hereby declare that all statements made herein on my own knowledge are true and that all statements made on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both under 18 USC §1001 and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Full Name of First Inventor: Karlheinz MÜLLER	
Inventor's Signature:	Date:
Residence: Finkenweg 11, D-84149 Velden, Germany	
Citizenship: German	·
Mailing Address: Finkenweg 11, D-84149 Velden, Germany	

Full Name of Second Inventor: Johannes Karl STURM

Inventor's Signature:

Residence: Franz-Pehr-Str. 7 B, A-9500 Villach, Austria

Citizenship: Austrian

Mailing Address: Franz-Pehr-Str. 7 B, A-9500 Villach, Austria



DECLARATION FOR PATENT APPLICATION, POWER OF ATTORNEY & DESIGNATION OF CORRESPONDENCE ADDRESS

As below named inventor, I hereby declare that:

patentability of this application in accordance with 37 CFR §1.56(a).

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

METHOD FOR FABRICATING AN INTEGRATED PIN DIODE AND ASSOCIATED CIRCUIT ARRANGEMENT

the specification of which (check one)
X is attached hereto.
was filed on as Application Serial No
and was amended on (if applicable).
I hereby state that I have reviewed and understand the contents of the above- identified specification including the claims, as amended by any amendment referred to above.
I acknowledge the duty to disclose information known to me to be material to the

PRIOR FOREIGN/PCT APPLICATION(S) FILED WITHIN 12 MONTHS (6 MONTHS FOR DESIGN) PRIOR TO THIS APPLICATION AND ANY PRIORITY CLAIMS UNDER 35 U.S.C. § 119(a)-(d)

I hereby claim foreign priority benefits under 35 USC § 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

No

CLAIM FOR BENEFIT OF PRIOR U.S. PROVISIONAL APPLICATION(S) (35 U.S.C. § 119(e))

I hereby claim the benefit under Title 35, United States Code, § 119 (e) of any United States provisional application(s) listed below:

Provisional Application Number	Filing Date

CLAIM FOR BENEFIT OF EARLIER US/PCT APPLICATION(S) UNDER 35 U.S.C. § 120

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Appl. No.	Filing Date	Patented, Pending, Abandoned
PCT/DE03/02740	August 14, 2003	Pending

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Customer No. 48581- Infineon Technologies

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Anthony P. Curtis, Ph.D. Tel.: (312) 321-4200; Fax: (312) 321-4299

I hereby declare that all statements made herein on my own knowledge are true and that all statements made on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both under 18 USC §1001 and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Full Name of First Inventor: Karlheinz MÜLLER			
Inventor's Signature: Weller The	Date: 25.2,2005		
Residence: Finkenweg 11, D-84149 Velden, Germany			
Citizenship: German			
Mailing Address: Finkenweg 11, D-84149 Velden, Germany			

Full Name of Second Inventor: Johannes Karl STURM			
Inventor's Signature:	Date:		
Residence: Franz-Pehr-Str. 7 B, A-9500 Villach, Austria			
Citizenship: Austrian			
Mailing Address: Franz-Pehr-Str. 7 B, A-9500 Villach, Austria			